

<b>Notice of References Cited</b>	Application/Control No. 10/761,034	Applicant(s)/Patent Under Reexamination VAN DOREN ET AL.	
	Examiner Paul A. Baker	Art Unit 2188	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2005/0053057 A1	03-2005	Deneroff et al.	370/360
*	B	US-2005/0013294 A1	01-2005	Cypher, Robert E.	370/389
*	C	US-2004/0123052 A1	06-2004	Beers et al.	711/150
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Scheurich et al., "The design of a lockup-free cache for high-performance multiprocessors", 14-18 Nov. 1988 Supercomputing '88. [Vol.1]. Proceedings. Page(s): 352 - 359
	V	Laudon et al., "The SGI Origin: a ccNUMA highly scalable server", 1997 International Conference on Computer Architecture Pages: 241 - 251
	W	Handy, "The Cache Memory Book", 1998, Academic Press 2nd ed, pgs 144-155
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.